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Serial Number: **10/815,950**

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 **PALM INTRANET**

Inventor Information for 10/815950

Inventor Name	City	State/Country
KURODA, AKIHIRO	ANJO-SHI	JAPAN

Appln. Info	Contents	Petition Info	Atty/Agent Info	Continuity/Reexam	Foreign I
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US 7187449 B2	20070306	Light-receiving/emitting composite unit, method for manufacturing the same, and displacement detection device	356/494		Tamiya; Hideaki et al.
US 7136241 B2	20061114	Information writing device	360/51	360/75	Taniguchi; Kayoko et al.
US 7034948 B2	20060425	Displacement pickup	356/499	250/231.16; 250/237R; 356/616	Tamiya; Hideaki et al.
US 6980303 B2	20051227	Light-emitting/receiving combined unit and displacement sensors using the same	356/614	356/616	Kume; Hidehiro et al.
US 6895936 B2	20050524	Common rail type fuel injection system	123/446	123/458; 123/467; 123/514; 123/516	Kuroda; Akihiro
US 6723667 B1	20040420	Pack preparation	442/123	156/247; 424/402; 424/443; 424/447; 428/520; 428/522; 442/394; 442/396; 524/703	Saito; Masato et al.
US 6722864 B2	20040420	Fuel injection pump	417/470	123/456; 123/495; 74/567; 74/596; 92/153; 92/72	Suzuki; Masashi et al.
US 6289875 B1	20010918	Fuel injection pump	123/450	123/495; 417/273	Shinohara; Yukihiro et al.
US 6184855 B1	20010206	Liquid crystal display panel driving device	345/100	345/94; 345/96	Kobayashi; Yoshinao et al.
US	20001226	Optical	356/499		Kuroda;

6166817 A		displacement measurement system for detecting the relative movement of a machine part			Akihiro
US 6151695 A	20001121	Test method of chips in a semiconductor wafer employing a test algorithm	714/734	714/741	Kamo; Yoshitaka et al.
US 6132743 A	20001017	Zinc oxide powder with suppressed activity and cosmetic preparation	424/401	106/425; 106/426; 106/429; 423/622; 424/489; 424/59; 424/642	Kuroda; Akihiro et al.
US 6094192 A	20000725	Common electrode driving device in a liquid crystal display	345/210	345/211; 345/212; 345/214	Kobayashi; Yoshinao et al.
US 5932891 A	19990803	Semiconductor device with test terminal and IC socket	257/48	257/737; 257/E23.069; 324/755; 361/767; 438/18; 439/66	Higashi; Tatsushi et al.
US 5353377 A	19941004	Speech recognition system having an interface to a host computer bus for direct access to the host memory	704/256.1	704/251	Kuroda; Akihiro et al.
US 4829577 A	19890509	Speech recognition method	704/244	704/256	Kuroda; Akihiro et al.
US 4774688 A	19880927	Data processing system for determining min/max in a single operation cycle as a result of a single	708/207		Kobayashi; Makoto et al.

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